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Embedded Memories for Nano-Scale VLSIs

In modern microprocessors and systems-on-a-chip, the embedded memory system plays a key role in determining the design's overall performance, power, area, reliability, and yield. As fabrication process technologies scale into the deep nanometer regime, increasing device variability poses particular difficulties for memory design due to the large number of variation-sensitive near minimum ...

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Weebit Nano appoints Ilan Sever to drive R&D | NewsReleases

thereof. For quantum memories, and quantum computing devices, |0 and |1 typically correspond to states of two different energies in either a natural "atomic system" (e.g. ion traps [27], NV centres in diamond [60], neutral atoms [8] or atomic ensembles [51]), or artificially designed nano-scale systems (e.g. superconducting quan-

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Listed semiconductor company Weebit Nano (ASX: WBT) has appointed non-volatile memory design expert Mr Ilan Sever as Vice President Research & Development to drive the company's progress within the embedded, standalone and neuromorphic markets. Mr Sever brings more than 25 years' in Very Large-Scale Integration (VLSI) design to Weebit Nano with extensive experience across the entire ...

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