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A simplified version of FPGA based design flow is given in the following diagram. Fig. 1: Flow Chart for FPGA

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Implementation Lets consider an example to illustrate the FPGA implementation procedure. The circuit shown in Fig. 2 is considered as a test circuit.

~~FPGA IMPLEMENTATION - Step By Step - Digital System Design~~

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FPGA-Based System Design . 2004.
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Attacks and Defenses on FPGA-based
Systems, ACM Transactions on
Reconfigurable Technology and
Systems, 12:3, (1-24), Online
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dimension has to do with FPGA-PCB co-design, which involves ensuring that pin-assignment is optimized for the PCB.

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Field-programmable gate array prototyping (FPGA prototyping), also referred to as FPGA-based prototyping, ASIC prototyping or system-on-chip (SoC) prototyping, is the method to prototype system-on-chip and application-specific integrated circuit designs on FPGAs for hardware

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verification and early software development.

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A field-programmable gate array (FPGA) is an integrated circuit designed to be configured by a customer or a designer after

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manufacturing – hence the term "field-programmable". The FPGA configuration is generally specified using a hardware description language (HDL), similar to that used for an application-specific integrated circuit (ASIC). Circuit diagrams were previously used to specify ...

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